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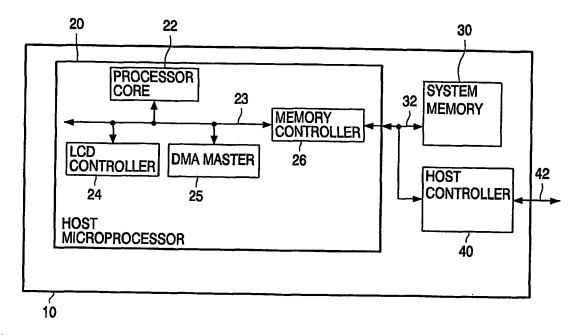
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(54) Title: USB HOST CONTROLLER WITH MEMORY FOR TRANSFER DESCRIPTORS



(57) Abstract: An electronic device, operating as a USB host, has an embedded processor and a system memory, connected by a memory bus. A host controller integrated circuit does not need to master the system memory, but instead acts purely as a slave. The embedded processor is then adapted to write the data to the host controller integrated circuit in the form of transfer-based transactions.

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